
Digital Design Frank Vahid 2nd Edition

A Unified Hardware/Software Introduction

Digital Logic Design Using Verilog

Digital Design, Preview Ed.

Digital Design

Electronic Devices and Circuit Applications

Advanced HDL Synthesis and SOC Prototyping

Java Concepts

Coding and RTL Synthesis

Readings in Hardware/software Co-design

Effective Coding with VHDL

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Capacitor; MOS Transistor; MOSFETs in
ICs—Scaling, Leakage, and Other Topics;
Bipolar Transistor. MARKET: Written by

an experienced teacher, researcher, and expert in industry practices, this succinct and forward-looking text is appropriate for anyone interested in semiconductor devices for integrated circuits, and serves as a suitable reference text for practicing engineers.

Digital Design, Preview Ed. Addison-Wesley Professional

This book is designed to serve as a hands-on professional reference with additional utility as a textbook for upper undergraduate and some graduate courses in digital logic design. This book is organized in such a way that that it can describe a number of RTL design scenarios, from simple to complex. The book constructs the logic design story from the fundamentals of logic design to advanced RTL design concepts. Keeping

in view the importance of miniaturization today, the book gives practical information on the issues with ASIC RTL design and how to overcome these concerns. It clearly explains how to write an efficient RTL code and how to improve design performance. The book also describes advanced RTL design concepts such as low-power design, multiple clock-domain design, and SOC-based design. The practical orientation of the book makes it ideal for training programs for practicing design engineers and for short-term vocational programs. The contents of the book will also make it a useful read for students and hobbyists.

Digital Design McGraw-Hill Higher Education

Java Concepts: Late Objects, 3rd Edition

focuses on the essentials of effective learning and is suitable for a two-semester introduction to programming sequence. This text requires no prior programming experience and only a modest amount of high school algebra. It provides an approachable introduction to fundamental programming techniques and design skills, helping students master basic concepts and become competent coders. Each important concept is introduced in easy-to-understand terms before more complicated examples are discussed. Choosing the enhanced eText format allows students to develop their coding skills using targeted, progressive interactivities designed to integrate with the eText. All sections include built-in activities, open-ended review exercises,

programming exercises, and projects to help students practice programming and build confidence. These activities go far beyond simplistic multiple-choice questions and animations. They have been designed to guide students along a learning path for mastering the complexities of programming. Students demonstrate comprehension of programming structures, then practice programming with simple steps in scaffolded settings, and finally write complete, automatically graded programs. The perpetual access VitalSource Enhanced eText, when integrated with your school's learning management system, provides the capability to monitor student progress in VitalSource SCORECenter and track grades for homework or participation.

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Electronic Devices and Circuit Applications Springer

With over 30 years of experience in both industrial and university settings, the author covers the most widespread logic design practices while building a solid foundation of theoretical and engineering principles for students to use as they go forward in this fast moving field.

Advanced HDL Synthesis and SOC Prototyping McGraw Hill Professional

This book is designed to facilitate a thorough understanding of fundamental principles without requiring readers to memorize an excess of confusing

technological details. Rather than focusing on techniques for one particular phase of design, it covers the complete design process, from specification to manufacturing.

Java Concepts "O'Reilly Media, Inc."

* Ideal as either a standalone introductory guide or in tandem with Vahid's Digital Design to allow for greater language coverage, this is an accessible introductory guide to hardware description language * Verilog is a hardware description language used to model electronic systems (sometimes called Verilog HDL) and this book is helpful for anyone who is starting out and learning the language * Focuses on application and use of the language, rather than just teaching the basics of the language

Coding and RTL Synthesis Pearson

A guide to applying software design principles and coding practices to VHDL to improve the readability, maintainability, and quality of VHDL code. This book addresses an often-neglected aspect of the creation of VHDL designs. A VHDL description is also source code, and VHDL designers can use the best practices of software development to write high-quality code and to organize it in a design. This book presents this unique set of skills, teaching VHDL designers of all experience levels how to apply the best design principles and coding practices from the software world to the world of hardware. The concepts introduced here will help readers write code that is easier to understand and more likely to be

correct, with improved readability, maintainability, and overall quality. After a brief review of VHDL, the book presents fundamental design principles for writing code, discussing such topics as design, quality, architecture, modularity, abstraction, and hierarchy. Building on these concepts, the book then introduces and provides recommendations for each basic element of VHDL code, including statements, design units, types, data objects, and subprograms. The book covers naming data objects and functions, commenting the source code, and visually presenting the code on the screen. All recommendations are supported by detailed rationales. Finally, the book explores two uses of VHDL: synthesis and testbenches. It examines

the key characteristics of code intended for synthesis (distinguishing it from code meant for simulation) and then demonstrates the design and implementation of testbenches with a series of examples that verify different kinds of models, including combinational, sequential, and FSM code. Examples from the book are also available on a companion website, enabling the reader to experiment with the complete source code.

Readings in Hardware/software Co-design Wiley

Digital Design provides a modern approach to learning the increasingly important topic of digital systems design. The text's focus on register-transfer-level design and present-day applications not only leads to a better

appreciation of computers and of today's ubiquitous digital devices, but also provides for a better understanding of careers involving digital design and embedded system design.1.

Introduction2. Combinational Logic Design3. Sequential Logic Design-Controllers4. Datapath Components5. Register-Transfer Level (RTL) Design6. Optimizations and Tradeoffs7. Physical Implementation8. Programmable Processors9. Hardware Description Languages

Effective Coding with VHDL Prentice Hall Professional

This book introduces a modern approach to embedded system design, presenting software design and hardware design in a unified manner. It covers trends and challenges, introduces the design and

use of single-purpose processors ("hardware") and general-purpose processors ("software"), describes memories and buses, illustrates hardware/software tradeoffs using a digital camera example, and discusses advanced computation models, controls systems, chip technologies, and modern design tools. For courses found in EE, CS and other engineering departments.

Digital Design Second Edition with Rtl Design, Vhdl, and Verilog Wiley E-Text Reg Card Morgan & Claypool Publishers

This is a practical book for computer engineers who want to understand or implement hardware/software systems. It focuses on problems that require one to combine hardware design with software design – such problems can be solved with hardware/software codesign.

When used properly, hardware/software co- sign works better than hardware design or software design alone: it can improve the overall performance of digital systems, and it can shorten their design time. Hardware/software codesign can help a designer to make trade-offs between the flexibility and the performance of a digital system. To achieve this, a designer needs to combine two radically different ways of design: the sequential way of decomposition in time, using software, with the parallel way of decomposition in space, using hardware.

Intended Audience This book assumes that you have a basic understanding of hardware that you are familiar with standard digital hardware components such as registers, logic gates, and components such as

multiplexers and arithmetic operators. The book also assumes that you know how to write a program in C. These topics are usually covered in an introductory course on computer engineering or in a combination of courses on digital design and software engineering.

Verilog for Digital Design Jossey-Bass

The purpose of this book is to introduce VHSIC Hardware Description Language (VHDL) and its use for synthesis. VHDL is a hardware description language which provides a means of specifying a digital system over different levels of abstraction. It supports behavior specification during the early stages of a design process and structural specification during the later implementation stages. VHDL was

originally introduced as a hardware description language that permitted the simulation of digital designs. It is now increasingly used for design specifications that are given as the input to synthesis tools which translate the specifications into netlists from which the physical systems can be built. One problem with this use of VHDL is that not all of its constructs are useful in synthesis. The specification of delay in signal assignments does not have a clear meaning in synthesis, where delays have already been determined by the implementation technology. VHDL has data-structures such as files and pointers, useful for simulation purposes but not for actual synthesis. As a result synthesis tools accept only subsets of VHDL. This book tries to cover the

synthesis aspect of VHDL, while keeping the simulation-specifics to a minimum. This book is suitable for working professionals as well as for graduate or under graduate study. Readers can view this book as a way to get acquainted with VHDL and how it can be used in modeling of digital designs.

Embedded System Design Pearson Education India

Shows developers how COM operates and how to use it to create efficient and stable programs consistent with the COM philosophy, allowing disparate applications and components to work together across a variety of languages, platforms, and host machines. Original. (Advanced).

Digital Design 2nd Edition with RTL Design, VHDL, and Verilog and

VHDL for Digital Design Set John Wiley & Sons

This text offers a comprehensive and balanced introduction to the design of small embedded systems. Important topics covered include microcontroller architectures, memory technologies, data conversion, serial protocols, program design, low power design, and design for the real time environment. The final chapter applies systematic engineering design principles to embedded system design. While the Microchip PIC 16F84 is used extensively to illustrate the early material, examples elsewhere are drawn from a range of microcontroller families, leading to a broad view of device capabilities.

Programming Embedded Systems Palgrave Macmillan

Master FPGA digital system design and implementation with Verilog and VHDL. This practical guide explores the development and deployment of FPGA-based digital systems using the two most popular hardware description languages, Verilog and VHDL. Written by a pair of digital circuit design experts, the book offers a solid grounding in FPGA principles, practices, and applications and provides an overview of more complex topics. Important concepts are demonstrated through real-world examples, ready-to-run code, and inexpensive start-to-finish projects for both the Basys and Arty boards. Digital System Design with FPGA: Implementation Using Verilog and VHDL covers:

- Field programmable gate array fundamentals
- Basys and Arty FPGA

- boards
- The Vivado design suite
- Verilog and VHDL
- Data types and operators
- Combinational circuits and circuit blocks
- Data storage elements and sequential circuits
- Soft-core microcontroller and digital interfacing
- Advanced FPGA applications
- The future of FPGA

Late Objects Wiley

This book, *Electronic Devices and Circuit Application*, is the first of four books of a larger work, *Fundamentals of Electronics*. It is comprised of four chapters describing the basic operation of each of the four fundamental building blocks of modern electronics: operational amplifiers, semiconductor diodes, bipolar junction transistors, and field effect transistors. Attention is focused on the reader obtaining a clear

understanding of each of the devices when it is operated in equilibrium. Ideas fundamental to the study of electronic circuits are also developed in the book at a basic level to lessen the possibility of misunderstandings at a higher level. The difference between linear and non-linear operation is explored through the use of a variety of circuit examples including amplifiers constructed with operational amplifiers as the fundamental component and elementary digital logic gates constructed with various transistor types. Fundamentals of Electronics has been designed primarily for use in an upper division course in electronics for electrical engineering students. Typically such a course spans a full academic year consisting of two semesters or three quarters. As such, Electronic

Devices and Circuit Applications, and the following two books, Amplifiers: Analysis and Design and Active Filters and Amplifier Frequency Response, form an appropriate body of material for such a course. Secondary applications include the use in a one-semester electronics course for engineers or as a reference for practicing engineers.

Out-of-order Parallel Discrete Event Simulation for Electronic System-level Design Springer

CD-ROM contains: Xilinx student edition foundation series software.

Third Edition John Wiley & Sons

This book, Amplifiers: Analysis and Design, is the second of four books of a larger work, Fundamentals of Electronics. It is comprised of four chapters that describe the fundamentals

of amplifier performance. Beginning with a review of two-port analysis, the first chapter introduces the modeling of the response of transistors to AC signals. Basic one-transistor amplifiers are extensively discussed. The next chapter expands the discussion to multiple transistor amplifiers. The coverage of simple amplifiers is concluded with a chapter that examines power amplifiers. This discussion defines the limits of small-signal analysis and explores the realm where these simplifying assumptions are no longer valid and distortion becomes present. The final chapter concludes the book with the first of two chapters in Fundamental of Electronics on the significant topic of feedback amplifiers. Fundamentals of Electronics has been designed primarily

for use in an upper division course in electronics for electrical engineering students. Typically such a course spans a full academic years consisting of two semesters or three quarters. As such, Amplifiers: Analysis and Design, and two other books, Electronic Devices and Circuit Applications, and Active Filters and Amplifier Frequency Response, form an appropriate body of material for such a course. Secondary applications include the use with Electronic Devices and Circuit Applications in a one-semester electronics course for engineers or as a reference for practicing engineers.

Verilog HDL John Wiley & Sons Incorporated

Simon introduces the broad range of applications for embedded software and then reviews each major issue facing

developers, offering practical solutions, techniques, and good habits that apply no matter which processor, real-time operating systems, methodology, or application is used.

Specification and Design of Embedded Systems Addison-Wesley Professional

"Digital Design provides a modern approach to learning the increasingly important topic of digital systems design. The text's focus on register-transfer-level design and present-day applications not only leads to a better appreciation of computers and of today's ubiquitous digital devices, but also provides for a better understanding of careers involving digital design and embedded system design. The book's key features include: An emphasis on

register-transfer-level (RTL) design, the level at which most digital design is practiced today, giving readers a modern perspective of the field's applicability. Yet, coverage stays bottom-up and concrete, starting from basic transistors and gates, and moving step-by-step up to more complex components. Extensive use of basic examples to teach and illustrate new concepts, and of application examples, such as pacemakers, ultrasound machines, automobiles, and cell phones, to demonstrate the immediate relevance of the concepts. Separation of basic design from optimization, allowing development of a solid understanding of basic design, before considering the more advanced topic of optimization. Flexible organization, enabling early or

late coverage of optimization methods or of HDLs, and enabling choice of VHDL, Verilog, or SystemC HDLs. Career insights and advice from designers with varying levels of experience. A clear bottom-up description of field-programmable gate arrays (FPGAs).
About the Author: Frank Vahid is a Professor of Computer Science & Engineering at the University of California, Riverside. He holds Electrical

Engineering and Computer Science degrees; has worked/consulted for Hewlett Packard, AMCC, NEC, Motorola, and medical equipment makers; holds 3 U.S. patents; has received several teaching awards; helped setup UCR's Computer Engineering program; has authored two previous textbooks; and has published over 120 papers on digital design topics (automation, architecture, and low-power).

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