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EX 1 ||DIGITAL SIGNAL || Particular Solution of First Order Difference Equation : $y(n) + ay(n-1) = x(n)$ **Q. 5.1: The D latch of Fig. 5.6 is constructed with four NAND gates and an inverter. Consider the**

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1.4 14-bit binary: 11_1111_1111_ Decimal: 2¹⁴ - 1 = 16,383 10 Hexadecimal: 3FFF 16. 1.5 Let b = base. (a) 14/2 = (b + 4)/2 = 5, so b = 6. (b) 54/4 = (5*b + 4)/4 = b + 3, so 5 * b = 52 - 4, and b = 8. (c) (2 *b + 4) + (b + 7) = 4b, so b = 11. 1.6 (x - 3) (x - 6) = x² - (6 + 3)x + 6*3 = x² - 11x + 22.

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[EX 1 ||DIGITAL SIGNAL || Particular Solution of First Order](#)

Difference Equation : $y(n) + ay(n-1) = x(n)$ [Q. 5.1: The D latch of Fig. 5.6 is constructed with four NAND gates and an inverter. Consider the](#)

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a variety of digital applications.

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$$1.6. (x - 3)(x - 6) = x^2 - (6 + 3)x + 6 \cdot 3 = x^2 - 11x + 22$$

Therefore: $6 + 3 = b + 1m$, so $b = 8$ Also, $6 \cdot 3 = (18)_{10} = (22)_8$.

$$1.7. 64CD_{16} = 0110_0100_1100_1101_2 = 110_010_011_001_101_2 = (62315)_{10} \dots$$