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 Modern System-on-Chip Design on Arm
 Principles and Industrial Applications
 Linux: Embedded Development
 The Real Time Kernel
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Advanced Black Magic "O'Reilly Media, Inc."

Designing a complex ASIC/SoC is similar to learning a new language to start with and ultimately creating a masterpiece using experience, imagination, and creativity. Digital design starts with RTL such as Verilog or VHDL, but it is only the beginning. A complete designer needs to have a good understanding of the Verilog language, digital design techniques, system architecture, IO protocols, and hardware-software interaction. Some of it will come from experience, and some will come with concerted effort. Graduating from college and entering into the world of digital system design becomes an overwhelming task, as not all the information is readily available. In this book, we have made an effort to explain the concepts in a simple way with real-world examples in Verilog. The book is intended for digital and system design engineers with emphasis on design and system architecture. The book is broadly divided into two sections - chapters 1 through 10, focusing on the digital design aspects and chapters 11 through 20, focusing on the system aspects of chip design. This book can be used by students taking digital design and chip design courses in college and availing it as a guide in their professional careers. Chapter 3 focuses on the synthesizable Verilog constructs, with examples on reusable design (parameterized design, functions, and generate structure). Chapter 5 describes the basic concepts in digital design - logic gates, truth table, De Morgan's theorem, set-up and hold time, edge detection, and number system. Chapter 6 goes into details of digital design explaining larger building blocks such as LFSR, scrambler/descramblers, error detection and correction, parity, CRC, Gray encoding/decoding, priority encoders, 8b/10b encoding, data converters, and synchronization techniques. Chapter 7 and 8 bring in advanced concepts in chip design and architecture - clocking and reset strategy, methods to increase throughput and reduce latency, flow-control mechanisms, pipeline operation, out-of-order execution, FIFO design, state machine design, arbitration, bus interfaces, linked list structure, and LRU usage and implementation. Chapter 9 and 10 describe how to build and design ASIC/SoC. It talks about chip micro-architecture, partitioning, datapath, control logic design, and other aspects of chip design such as clock tree, reset tree, and EEPROM. It also covers good design practices, things to avoid and adopt, and best practices for high-speed design. The second part of the book is devoted to System architecture, design, and IO protocols. Chapter 11 talks about memory, memory hierarchy, cache, interrupt, types of DMA and DMA operation. There is Verilog RTL for a typical DMA controller design that explains the scatter-gather DMA concept. Chapter 12 describes hard drive, solid-state drive, DDR operation, and other parts of a system such as BIOS, OS, drivers, and their interaction with hardware. Chapter 13 describes embedded systems and internal buses such as AHB, AXI used in embedded design. It describes the concept of transparent and non-transparent bridging. Chapter 14 and chapter 15 bring in practical aspects of chip development - testing, DFT, scan, ATPG, and detailed flow of the chip development cycle (Synthesis, Static timing, and ECO). Chapter 16 and chapter 17 are on power saving and power management protocols. Chapter 16 has a detailed description of various power savings techniques (frequency variation, clock gating, and power well isolation). Chapter 17 talks about Power Management protocols such as system S states, CPU C states, and device D states. Chapter 18 explains the architecture behind serial-bus technology, PCS, and PMA layer. It describes clocking architecture and advanced concepts such as elasticity FIFO, channel bonding (deskewing), link aggregation, and lane reversal. Chapter 19 and 20 are devoted to serial bus protocols (PCI Express, Serial ATA, USB, Thunderbolt, and Ethernet) and their operation. *Modern System-on-Chip Design on Arm* Springer Leverage the power of Linux to develop captivating and powerful embedded Linux projects About This Book Explore the best practices for all embedded product development stages Learn about the

compelling features offered by the Yocto Project, such as customization, virtualization, and many more Minimize project costs by using open source tools and programs Who This Book Is For If you are a developer who wants to build embedded systems using Linux, this book is for you. It is the ideal guide for you if you want to become proficient and broaden your knowledge. A basic understanding of C programming and experience with systems programming is needed. Experienced embedded Yocto developers will find new insight into working methodologies and ARM specific development competence. What You Will Learn Use the Yocto Project in the embedded Linux development process Get familiar with and customize the bootloader for a board Discover more about real-time layer, security, virtualization, CGL, and LSB See development workflows for the U-Boot and the Linux kernel, including debugging and optimization Understand the open source licensing requirements and how to comply with them when cohabiting with proprietary programs Optimize your production systems by reducing the size of both the Linux kernel and root filesystems Understand device trees and make changes to accommodate new hardware on your device Design and write multi-threaded applications using POSIX threads Measure real-time latencies and tune the Linux kernel to minimize them In Detail Embedded Linux is a complete Linux distribution employed to operate embedded devices such as smartphones, tablets, PDAs, set-top boxes, and many more. An example of an embedded Linux distribution is Android, developed by Google. This learning path starts with the module Learning Embedded Linux Using the Yocto Project. It introduces embedded Linux software and hardware architecture and presents information about the bootloader. You will go through Linux kernel features and source code and get an overview of the Yocto Project components available. The next module Embedded Linux Projects Using Yocto Project Cookbook takes you through the installation of a professional embedded Yocto setup, then advises you on best practices. Finally, it explains how to quickly get hands-on with the Freescale ARM ecosystem and community layer using the affordable and open source Wandboard embedded board. Moving ahead, the final module Mastering Embedded Linux Programming takes you through the product cycle and gives you an in-depth description of the components and options that are available at each stage. You will see how functions are split between processes and the usage of POSIX threads. By the end of this learning path, your capabilities will be enhanced to create robust and versatile embedded projects. This Learning Path combines some of the best that Packt has to offer in one complete, curated package. It includes content from the following Packt products: Learning Embedded Linux Using the Yocto Project by Alexandru Vaduva Embedded Linux Projects Using Yocto Project Cookbook by Alex Gonzalez Mastering Embedded Linux Programming by Chris Simmonds Style and approach This comprehensive, step-by-step, pragmatic guide enables you to build custom versions of Linux for new embedded systems with examples that are immediately applicable to your embedded developments. Practical examples provide an easy-to-follow way to learn Yocto project development using the best practices and working methodologies. Coupled with hints and best practices, this will help you understand embedded Linux better.

Principles and Industrial Applications Elsevier

Master the art of developing customized device drivers for your embedded Linux systems Key Features Stay up to date with the Linux PCI, ASoC, and V4L2 subsystems and write device drivers for them Get to grips with the Linux kernel power management infrastructure Adopt a practical approach to customizing your Linux environment using best practices Book Description Linux is one of the fastest-growing operating systems around the world, and in the last few years, the Linux kernel has evolved significantly to support a wide variety of embedded devices with its improved subsystems and a range of new features. With this book, you'll find out how you can enhance your skills to write custom device drivers for your Linux operating system. Mastering Linux Device Driver Development provides complete coverage of kernel topics, including video and audio frameworks, that usually go unaddressed. You'll work with some of the most complex and impactful Linux kernel

frameworks, such as PCI, ALSA for SoC, and Video4Linux2, and discover expert tips and best practices along the way. In addition to this, you'll understand how to make the most of frameworks such as NVMEM and Watchdog. Once you've got to grips with Linux kernel helpers, you'll advance to working with special device types such as Multi-Function Devices (MFD) followed by video and audio device drivers. By the end of this book, you'll be able to write feature-rich device drivers and integrate them with some of the most complex Linux kernel frameworks, including V4L2 and ALSA for SoC. What you will learn Explore and adopt Linux kernel helpers for locking, work deferral, and interrupt management Understand the Regmap subsystem to manage memory accesses and work with the IRQ subsystem Get to grips with the PCI subsystem and write reliable drivers for PCI devices Write full multimedia device drivers using ALSA SoC and the V4L2 framework Build power-aware device drivers using the kernel power management framework Find out how to get the most out of miscellaneous kernel subsystems such as NVMEM and Watchdog Who this book is for This book is for embedded developers, Linux system engineers, and system programmers who want to explore Linux kernel frameworks and subsystems. C programming skills and a basic understanding of driver development are necessary to get started with this book.

Linux: Embedded Development Newnes

SoC design has seen significant advances in the decade and Arm-based silicon has often been at the heart of this revolution. Today, entire systems including processors, memories, sensors and analogue circuitry are all integrated into one single chip (hence "System-on-Chip" or SoC). The aim of this textbook is to expose aspiring and practising SoC designers to the fundamentals and latest developments in SoC design and technologies using examples of Arm(R) Cortex(R)-A technology and related IP blocks and interfaces. The entire SoC design process is discussed in detail, from memory and interconnects through to validation, fabrication and production. A particular highlight of this textbook is the focus on energy efficient SoC design, and the extensive supplementary materials which include a SystemC model of a Zynq chip. This textbook is aimed at final year undergraduate students, master students or engineers in the field looking to update their knowledge. It is assumed that readers will have a pre-existing understanding of RTL, Assembly Language and Operating Systems. For those readers looking for an entry-level introduction to SoC design, we recommend our Fundamentals of System-on-Chip Design on Arm Cortex-M Microcontrollers textbook.

The Real Time Kernel Apress

The Definitive Guide to Arm® Cortex®-M23 and Cortex-M33 Processors focuses on the Armv8-M architecture and the features that are available in the Cortex-M23 and Cortex-M33 processors. This book covers a range of topics, including the instruction set, the programmer's model, interrupt handling, OS support, and debug features. It demonstrates how to create software for the Cortex-M23 and Cortex-M33 processors by way of a range of examples, which will enable embedded software developers to understand the Armv8-M architecture. This book also covers the TrustZone® technology in detail, including how it benefits security in IoT applications, its operations, how the technology affects the processor's hardware (e.g., memory architecture, interrupt handling, etc.), and various other considerations in creating secure software. Presents the first book on Armv8-M Architecture and its features as implemented in the Cortex-M23 and Cortex-M33 processors Covers TrustZone technology in detail Includes examples showing how to create software for Cortex-M23/M33 processors

Ciarcia's Circuit Cellar Createspace Independent Publishing Platform

A thorough revision that provides a clear understanding of the basic principles of microcontrollers using C programming and PIC18F assembly language This book presents the fundamental concepts of assembly language programming and interfacing techniques associated with typical microcontrollers. As part of the second edition's revisions, PIC18F assembly language and C programming are provided in separate sections so that these topics can be covered independent of each other if desired. This extensively updated edition includes a number of fundamental topics. Characteristics and principles common to typical microcontrollers are emphasized. Interfacing techniques associated with a basic microcontroller such as the PIC18F are demonstrated from chip level via examples using the simplest possible devices, such as switches, LEDs, Seven-Segment displays, and the hexadecimal keyboard. In addition, interfacing the PIC18F with other devices such as LCD displays, ADC, and DAC is also included. Furthermore, topics such as CCP (Capture, Compare, PWM) and Serial I/O using C along with simple examples are also provided. Microcontroller Theory and Applications with the PIC18F, 2nd Edition is a comprehensive and self-contained book that emphasizes characteristics and principles common to typical microcontrollers. In addition, the text: Includes increased coverage of C language programming with the PIC18F I/O and interfacing techniques Provides a more detailed explanation of PIC18F timers, PWM, and Serial I/O using C Illustrates C interfacing techniques through the use of numerous examples, most of which have been implemented successfully in the laboratory This new edition of Microcontroller Theory and Applications with the PIC18F is excellent as a text for undergraduate level students of electrical/computer engineering and computer science.

Learn to Develop Linux Embedded Drivers with Kernel 4.9 LTS Newnes

This book focuses on the principles of wireless sensor networks (WSNs), their applications, and their analysis tools, with meticulous attention paid to definitions and terminology. This book presents the adopted technologies and their manufacturers in detail, making WSNs tangible for the reader. In introductory computer networking books, chapter sequencing follows the bottom-up or top-down architecture of the 7-layer protocol. This book addresses subsequent steps in this process, both horizontally and vertically, thus fostering a clearer and deeper understanding through chapters that elaborate on WSN concepts and issues. With such depth, this book is intended for a wide audience; it is meant to be a helper and motivator for senior undergraduates, postgraduates, researchers, and practitioners. It lays out important concepts and WSN-relate applications; uses appropriate literature to back research and practical issues; and focuses on new trends. Senior undergraduate students can use it to familiarize themselves with conceptual foundations and practical project implementations. For graduate students and researchers, test beds and simulators provide vital insights into analysis methods and tools for WSNs. Lastly, in addition to applications and deployment, practitioners will be able to learn more about WSN manufacturers and components within several platforms and test beds.

Practical Examples in Verilog Springer Nature

The purpose of this book is to help new Harmony users climb the steep learning curve so they can use the MPLAB Harmony development environment to develop reliable and reproducible applications using Microchip devices. The "learn-by-doing" method used in the book provides a deep understanding of the underlying structures and architecture of Harmony which can be applied to the PIC32MZ (EF) starter kit and/or custom boards. Microchip provides a wide variety of hardware products supported by a comprehensive suite of software development tools. For newbies, however, choosing which tools and products can be overwhelming. The Microchip starter kits can be used to demonstrate the hardware and software capabilities, but the learning curve can be daunting. Even though Microchip provides wikidot tutorials and thousands of pages of documentation, there is little or no guidance to explain the "how and why" when developing Harmony applications. My first book, "Harmony for PIC32MX Applications," is based on the Ethernet Starter Kit II (MX chip) and targets the new-to-Harmony user. This book is also for a new Harmony user, but it addresses functionality

available with the more powerful MZ chip and includes features that are of interest to the intermediate-level developer. I have added new sections to this book that: (a) use the Pin Manager to connect the board peripherals to the application code, and (b) added an entire chapter that addresses how Harmony can be used in conjunction with the real-time operating system, FreeRTOS. For more detailed information, including the Table of Contents and Introduction chapters, go to www.caprtechnologybooks.com.

Linux Driver Development for Embedded Processors - Second Edition Springer Science & Business Media

The new edition of this popular book has been transformed into a hands-on textbook, focusing on the principles of wireless sensor networks (WSNs), their applications, their protocols and standards, and their analysis and test tools; a meticulous care has been accorded to the definitions and terminology. To make WSNs felt and seen, the adopted technologies as well as their manufacturers are presented in detail. In introductory computer networking books, chapters sequencing follows the bottom up or top down architecture of the seven layers protocol. This book starts some steps later, with chapters ordered based on a topic's significance to the elaboration of wireless sensor networks (WSNs) concepts and issues. With such a depth, this book is intended for a wide audience, it is meant to be a helper and motivator, for both the senior undergraduates, postgraduates, researchers, and practitioners; concepts and WSNs related applications are laid out, research and practical issues are backed by appropriate literature, and new trends are put under focus. For senior undergraduate students, it familiarizes readers with conceptual foundations, applications, and practical project implementations. For graduate students and researchers, transport layer protocols and cross-layering protocols are presented and testbeds and simulators provide a must follow emphasis on the analysis methods and tools for WSNs. For practitioners, besides applications and deployment, the manufacturers and components of WSNs at several platforms and testbeds are fully explored.

Embedded Systems Hardware for Software Engineers Packt Publishing Ltd

This updated and expanded version of the very successful first edition offers new chapters on controlling the emission from electronic systems, especially digital systems, and on low-cost techniques for providing electromagnetic compatibility (EMC) for consumer products sold in a competitive market. There is also a new chapter on the susceptibility of electronic systems to electrostatic discharge. There is more material on FCC regulations, digital circuit noise and layout, and digital circuit radiation. Virtually all the material in the first edition has been retained. Contains a new appendix on FCC EMC test procedures.

Mixed-Signal Embedded Systems Design Springer Science & Business Media

The use of microcontroller based solutions to everyday design problems in electronics, is the most important development in the field since the introduction of the microprocessor itself. The PIC family is established as the number one microcontroller at an introductory level. Assuming no prior knowledge of microprocessors, Martin Bates provides a comprehensive introduction to microprocessor systems and applications covering all the basic principles of microelectronics. Using the latest Windows development software MPLAB, the author goes on to introduce microelectronic systems through the most popular PIC devices currently used for project work, both in schools and colleges, as well as undergraduate university courses. Students of introductory level microelectronics, including microprocessor / microcontroller systems courses, introductory embedded systems design and control electronics, will find this highly illustrated text covers all their requirements for working with the PIC. Part A covers the essential principles, concentrating on a systems approach. The PIC itself is covered in Part B, step by step, leading to demonstration programmes using labels, subroutines, timer and interrupts. Part C then shows how applications may be developed using the latest Windows software, and some hardware prototyping methods. The new edition is suitable for a range of students and PIC enthusiasts, from beginner to first and second year undergraduate level. In the UK, the book is of specific relevance to AVCE, as well as BTEC National and Higher National programmes in electronic engineering. · A comprehensive introductory text in microelectronic systems, written round the leading chip for project work · Uses the latest Windows development software, MPLAB, and the most popular types of PIC, for accessible and low-cost practical work · Focuses on the 16F84 as the starting point for introducing the basic architecture of the PIC, but also covers newer chips in the 16F8X range, and 8-pin mini-PICs

Learn Harmony for Pic32mz CRC Press

MicroC/OS II Second Edition describes the design and implementation of the MicroC/OS-II real-time operating system (RTOS). In addition to its value as a reference to the kernel, it is an extremely detailed and highly readable design study particularly useful to the embedded systems student.

While documenting the design and implementation of the ker

Mastering Embedded Linux Programming Packt Publishing Ltd

LINUX DRIVER DEVELOPMENT FOR EMBEDDED PROCESSORS - SECOND EDITION - The flexibility of Linux embedded, the availability of powerful, energy efficient processors designed for embedded computing and the low cost of new processors are encouraging many industrial companies to come up with new developments based on embedded processors. Current engineers have in their hands powerful tools for developing applications previously unimagined, but they need to understand the countless features that Linux offers today. This book will teach you how to develop device drivers for Device Tree Linux embedded systems. You will learn how to write different types of Linux drivers, as well as the appropriate APIs (Application Program Interfaces) and methods to interface with kernel and user spaces. This is a book is meant to be practical, but also provides an important theoretical base. More than twenty drivers are written and ported to three different processors. You can choose between NXP i.MX7D, Microchip SAMA5D2 and Broadcom BCM2837 processors to develop and test the drivers, whose implementation is described in detail in the practical lab sections of the book. Before you start reading, I encourage you to acquire any of these processor boards whenever you have access to some GPIOs, and at least one SPI and I2C controllers. The hardware configurations of the different evaluation boards used to develop the drivers are explained in detail throughout this book; one of the boards used to implement the drivers is the famous Raspberry Pi 3 Model B board. You will learn how to develop drivers, from the simplest ones that do not interact with any external hardware, to drivers that manage different kind of devices: accelerometers, DACs, ADCs, RGB LEDs, Multi-Display LED controllers, I/O expanders, and Buttons. You will also develop DMA drivers, drivers that manage interrupts, and drivers that write/read on the internal registers of the processor to control external devices. To easy the development of some of these drivers, you will use different types of Frameworks: Miscellaneous framework, LED framework, UIO framework, Input framework and the IIO industrial one. This second edition has been updated to the v4.9 LTS kernel. Recently, all the drivers have been ported to the new Microchip SAMA5D27-SOM1 (SAMA5D27 System On Module) using kernel 4.14 LTS and included in the GitHub repository of this book; these drivers have been tested in the ATSAMA5D27-SOM1-EK1 evaluation platform; the ATSAMA5D27-SOM1-EK1 practice lab settings are not described throughout the text of this book, but in a practice labs user guide that can be downloaded from the book's GitHub.

Assertion-Based Design Springer Science & Business Media

In this new edition the latest ARM processors and other hardware developments are fully covered along with new sections on Embedded Linux and the new freeware operating system eCOS. The hot

topic of embedded systems and the internet is also introduced. In addition a fascinating new case study explores how embedded systems can be developed and experimented with using nothing more than a standard PC. * A practical introduction to the hottest topic in modern electronics design * Covers hardware, interfacing and programming in one book * New material on Embedded Linux for embedded internet systems

Embedded Systems Design Newnes

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with Interactive Hardware Simulation Elsevier

Presents an overview of kernel configuration and building for version 2.6 of the Linux kernel.

Concepts, Applications, Experimentation and Analysis of Wireless Sensor Networks

Morgan Kaufmann

Harness the power of Linux to create versatile and robust embedded solutions Key Features Learn how to develop and configure robust embedded Linux devices Explore the new features of Linux 5.4 and the Yocto Project 3.1 (Dunfell) Discover different ways to debug and profile your code in both user space and the Linux kernel Book Description Embedded Linux runs many of the devices we use every day. From smart TVs and Wi-Fi routers to test equipment and industrial controllers, all of them have Linux at their heart. The Linux OS is one of the foundational technologies comprising the core of the Internet of Things (IoT). This book starts by breaking down the fundamental elements that underpin all embedded Linux projects: the toolchain, the bootloader, the kernel, and the root filesystem. After that, you will learn how to create each of these elements from scratch and automate the process using Buildroot and the Yocto Project. As you progress, the book explains how to implement an effective storage strategy for flash memory chips and install updates to a device remotely once it's deployed. You'll also learn about the key aspects of writing code for embedded Linux, such as how to access hardware from apps, the implications of writing multi-threaded code, and techniques to manage memory in an efficient way. The final chapters demonstrate how to debug your code, whether it resides in apps or in the Linux kernel itself. You'll also cover the different tracers and profilers that are available for Linux so that you can quickly pinpoint any performance bottlenecks in your system. By the end of this Linux book, you'll be able to create efficient and secure embedded devices using Linux. What you will learn Use Buildroot and the Yocto Project to create embedded Linux systems Troubleshoot BitBake build failures and streamline your Yocto development workflow Update IoT devices securely in the field using Mender or balena Prototype peripheral additions by reading schematics, modifying device trees, soldering breakout boards, and probing pins with a logic analyzer Interact with hardware without having to write kernel device drivers Divide your system up into services supervised by BusyBox runit Debug devices remotely using GDB and measure the performance of systems using tools such as perf, ftrace, eBPF, and Callgrind Who this book is for If you're a systems software engineer or system administrator who wants to learn Linux implementation on embedded devices, then this book is for you. Embedded systems engineers accustomed to programming for low-power microcontrollers can use this book to help make the leap to high-speed systems on chips that can run Linux. Anyone responsible for developing new hardware that needs to run Linux will also find this book useful. Basic working knowledge of the POSIX standard, C programming, and shell scripting is assumed.

Australia's Place in the Asia Pacific Circuit Cellar

Master the techniques needed to build great, efficient embedded devices on Linux About This Book Discover how to build and configure reliable embedded Linux devices This book has been updated to

include Linux 4.9 and Yocto Project 2.2 (Morty) This comprehensive guide covers the remote update of devices in the field and power management Who This Book Is For If you are an engineer who wishes to understand and use Linux in embedded devices, this book is for you. It is also for Linux developers and system programmers who are familiar with embedded systems and want to learn and program the best in class devices. It is appropriate for students studying embedded techniques, for developers implementing embedded Linux devices, and engineers supporting existing Linux devices. What You Will Learn Evaluate the Board Support Packages offered by most manufacturers of a system on chip or embedded module Use Buildroot and the Yocto Project to create embedded Linux systems quickly and efficiently Update IoT devices in the field without compromising security Reduce the power budget of devices to make batteries last longer Interact with the hardware without having to write kernel device drivers Debug devices remotely using GDB, and see how to measure the performance of the systems using powerful tools such as perf, ftrace, and valgrind Find out how to configure Linux as a real-time operating system In Detail Embedded Linux runs many of the devices we use every day, from smart TVs to WiFi routers, test equipment to industrial controllers - all of them have Linux at their heart. Linux is a core technology in the implementation of the inter-connected world of the Internet of Things. The comprehensive guide shows you the technologies and techniques required to build Linux into embedded systems. You will begin by learning about the fundamental elements that underpin all embedded Linux projects: the toolchain, the bootloader, the kernel, and the root filesystem. You'll see how to create each of these elements from scratch, and how to automate the process using Buildroot and the Yocto Project. Moving on, you'll find out how to implement an effective storage strategy for flash memory chips, and how to install updates to the device remotely once it is deployed. You'll also get to know the key aspects of writing code for embedded Linux, such as how to access hardware from applications, the implications of writing multi-threaded code, and techniques to manage memory in an efficient way. The final chapters show you how to debug your code, both in applications and in the Linux kernel, and how to profile the system so that you can look out for performance bottlenecks. By the end of the book, you will have a complete overview of the steps required to create a successful embedded Linux system. Style and approach This book is an easy-to-follow and pragmatic guide with in-depth analysis of the implementation of embedded devices. It follows the life cycle of a project from inception through to completion, at each stage giving both the theory that underlies the topic and practical step-by-step walkthroughs of an example implementation.

Advanced Chip Design Springer Science & Business Media

About the ARM Architecture The ARM architecture is the industry's leading 16/32-bit embedded RISC processor solution. ARM Powered microprocessors are being routinely designed into a wider range of products than any other 32-bit processor. This wide applicability is made possible by the ARM architecture, resulting in optimal system solutions at the crossroads of high performance, low power consumption and low cost. About the book This is the authoritative reference guide to the ARM RISC architecture. Produced by the architects that are actively working on the ARM specification, the book contains detailed information about all versions of the ARM and Thumb instruction sets, the memory management and cache functions, as well as optimized code examples. 0201737191B05092001

System Design with SystemCTM Packt Publishing Ltd

This book provides a hands-on introductory course on concepts of C programming using a PIC® microcontroller and CCS C compiler. Through a project-based approach, this book provides an easy to understand method of learning the correct and efficient practices to program a PIC® microcontroller in C language. Principles of C programming are introduced gradually, building on skill sets and knowledge. Early chapters emphasize the understanding of C language through experience and exercises, while the latter half of the book covers the PIC® microcontroller, its peripherals, and how to use those peripherals from within C in great detail. This book demonstrates the programming methodology and tools used by most professionals in embedded design, and will enable you to apply your knowledge and programming skills for any real-life application. Providing a step-by-step guide to the subject matter, this book will encourage you to alter, expand, and customize code for use in your own projects. A complete introduction to C programming using PIC microcontrollers, with a focus on real-world applications, programming methodology and tools Each chapter includes C code project examples, tables, graphs, charts, references, photographs, schematic diagrams, flow charts and compiler compatibility notes to channel your knowledge into real-world examples Online materials include presentation slides, extended tests, exercises, quizzes and answers, real-world case studies, videos and weblinks

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